

1       **51550/LTR/B600**

ABSTRACT OF THE DISCLOSURE

5       Binary indications are converted to an analog  
representation with significant reduction in ringing at the  
transitions between successive binary indications and in the  
period during each binary indication. The binary indications  
are disposed in a row-and-column matrix to provide a  
10       thermometer code. Each stage of the converter includes a  
decoder and latch arranged so the decoder inputs settle before  
the latch is set by the clock pulses. The stages are  
implemented in complementary CMOS. Complementary transistors  
are biased so one transistor of the pair is driven to the rail  
15       while the other transistor of the pair floats. A dummy CMOS  
transistor is used to balance the number of transistors in the  
decoder paths.

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